

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

What is claimed is:

1. (Currently Amended) A method of manufacturing an electronic device, the method comprising:

forming an external terminal on an interconnect pattern formed on a substrate; ~~and~~

subsequently mounting a chip component on the substrate face up, the chip component having an electrode on a first surface of the chip component opposite to a second surface facing the substrate; having an electrode on the substrate, and

forming an interconnect for electrically connecting the electrode and the interconnect pattern at a temperature lower than a melting point of the external terminal.

2. (Original) The method of manufacturing an electronic device as defined by claim 1,

wherein the interconnect is formed of a dispersant including electrically conductive particles.

3. (Original) The method of manufacturing an electronic device as defined by claim 2, further comprising:

forming an insulating section adjacent to the chip component,  
wherein the step of forming the interconnect includes ejecting a dispersant including the electrically conductive particles over the insulating section and the interconnect pattern.

4. (Original) The method of manufacturing an electronic device as defined by claim 3, wherein the insulating section is formed of a resin.

5. (Original) The method of manufacturing an electronic device as defined by claim 3,

wherein the insulating section is formed to have an inclined surface descending in an outward direction from the chip component.

6. (Original) The method of manufacturing an electronic device as defined by claim 4,

wherein the insulating section is formed to have an inclined surface descending in an outward direction from the chip component.

7. (Original) The method of manufacturing an electronic device as defined by claim 1,

wherein the chip component is a semiconductor element.

8. (Withdrawn) An electronic device manufactured by the method as defined by claim 1.

9. (Withdrawn) A circuit board on which the electronic device as defined by claim 8 is mounted.

10. (Withdrawn) An electronic instrument having the electronic device as defined by claim 8.

11. (Withdrawn) A chip carrier comprising:

an external terminal formed of a soldering material, the external terminal being provided on an interconnect pattern formed on a substrate; and  
a region connected to an electrode which is included in a chip component.